

<b>Notice of References Cited</b>	Application/Control No. 10/828,910		Applicant(s)/Patent Under Reexamination NGUYEN ET AL.	
	Examiner Matthew D. Spittle		Art Unit 2111	Page 1 of 1

**U.S. PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
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*	B	US-2003/0007338	01-2003	Brosowski, Hans-Otto	361/785
*	C	US-2006/0098020	05-2006	Shen et al.	345/520
*	D	US-6,356,967	03-2002	Vaisanen et al.	710/300
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	K	US-			
	L	US-			
	M	US-			

**FOREIGN PATENT DOCUMENTS**

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**NON-PATENT DOCUMENTS**

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	NXP N.V. NXP 2-lane channel switch. CBTU0808. Datasheet. October 2006.
	V	Ngai, Henry. Lane Routing in PCI Express. Embedded Computing Design. September 2005.
	W	Bhatt, Ajay V. Creating a PCI Express Interconnect. White Paper. 2002.
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\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)  
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.